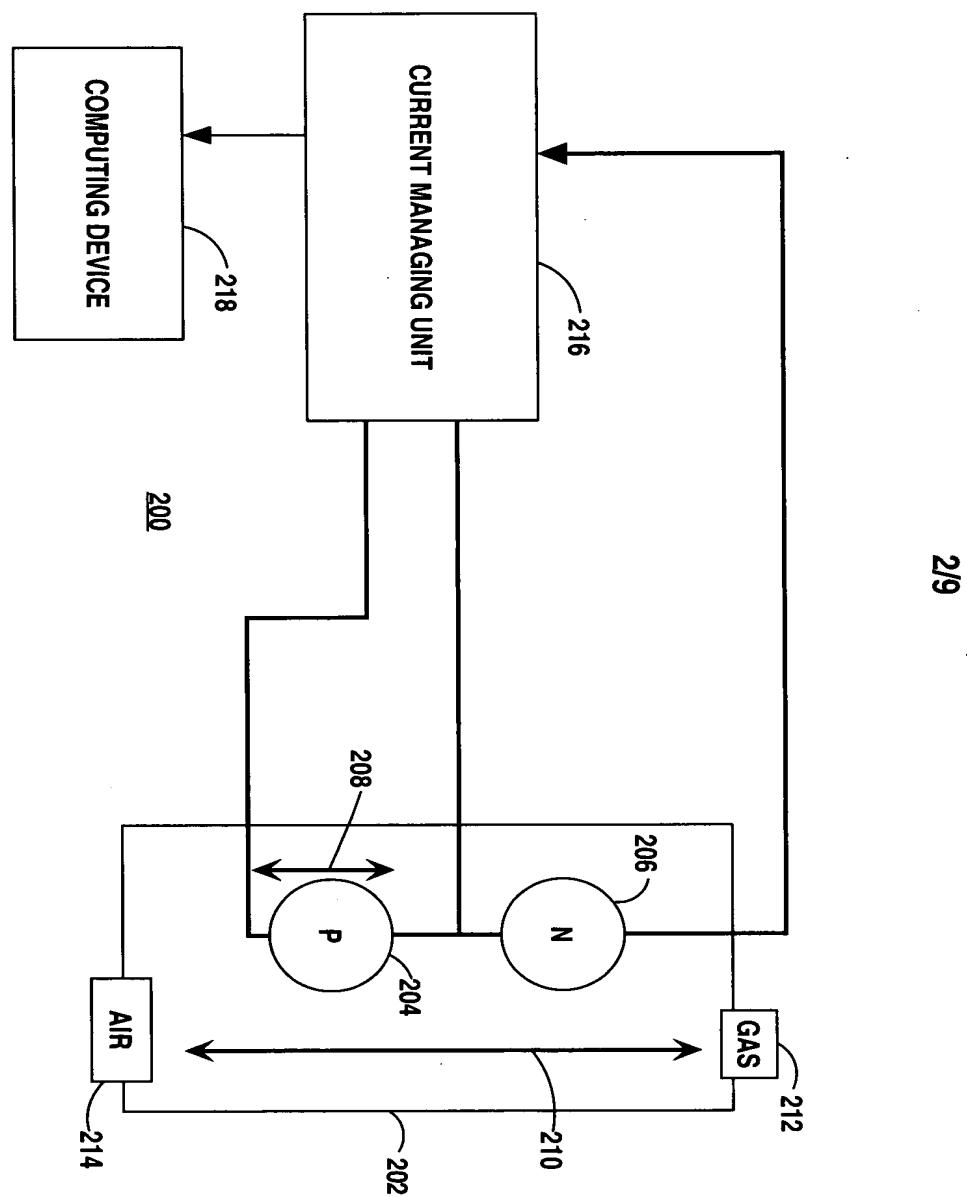


Fig. 1

Fig. 2



+

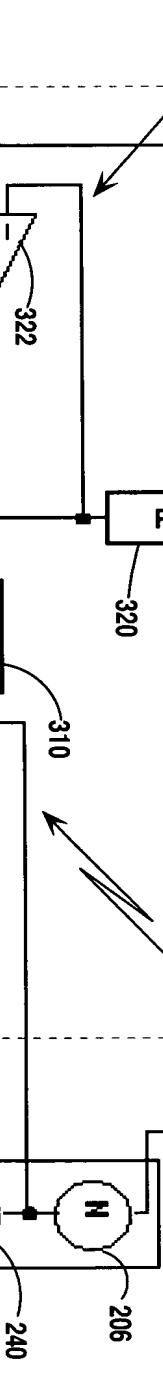
3/9

VCC

VCC

R3
320R1
324

304

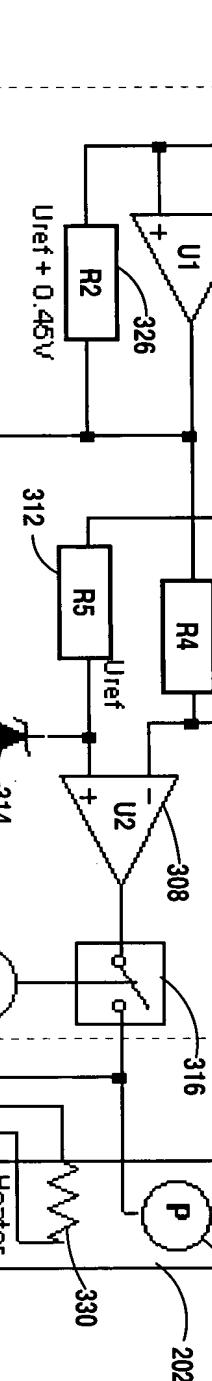
R4
310U2
308

312

316

314

306



Uref + 0.45V

R2
326R5
312

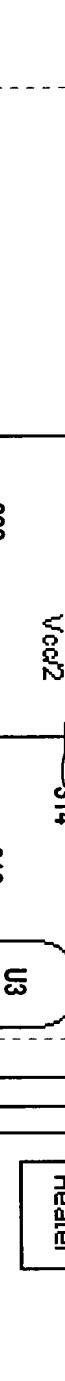
Uref

318

314

316

308

R5
312

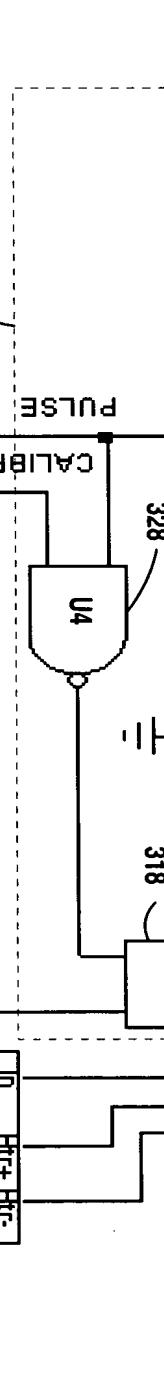
Uref

314

318

316

310

R5
312

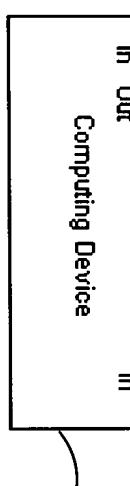
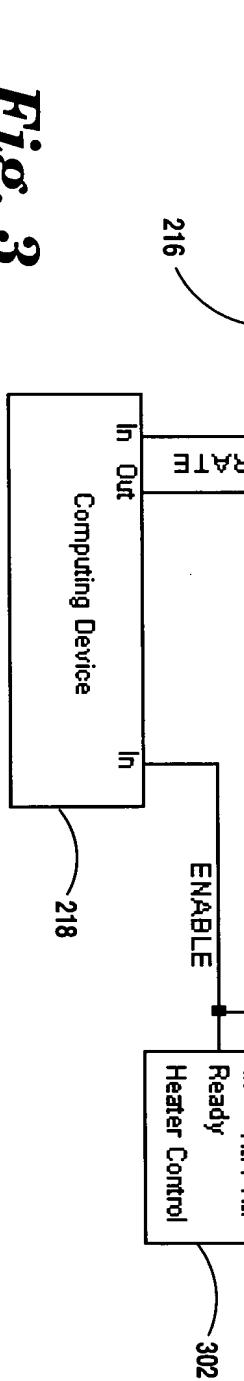
Uref

314

318

316

312

U4
328

328

318

316

312

302

Fig. 3

+

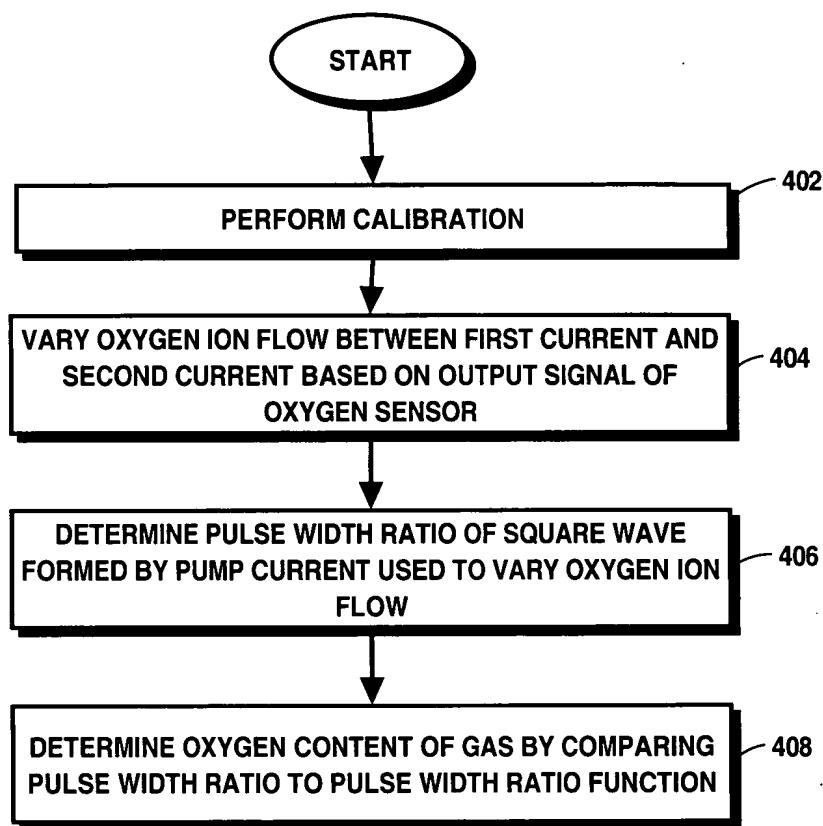


Fig. 4

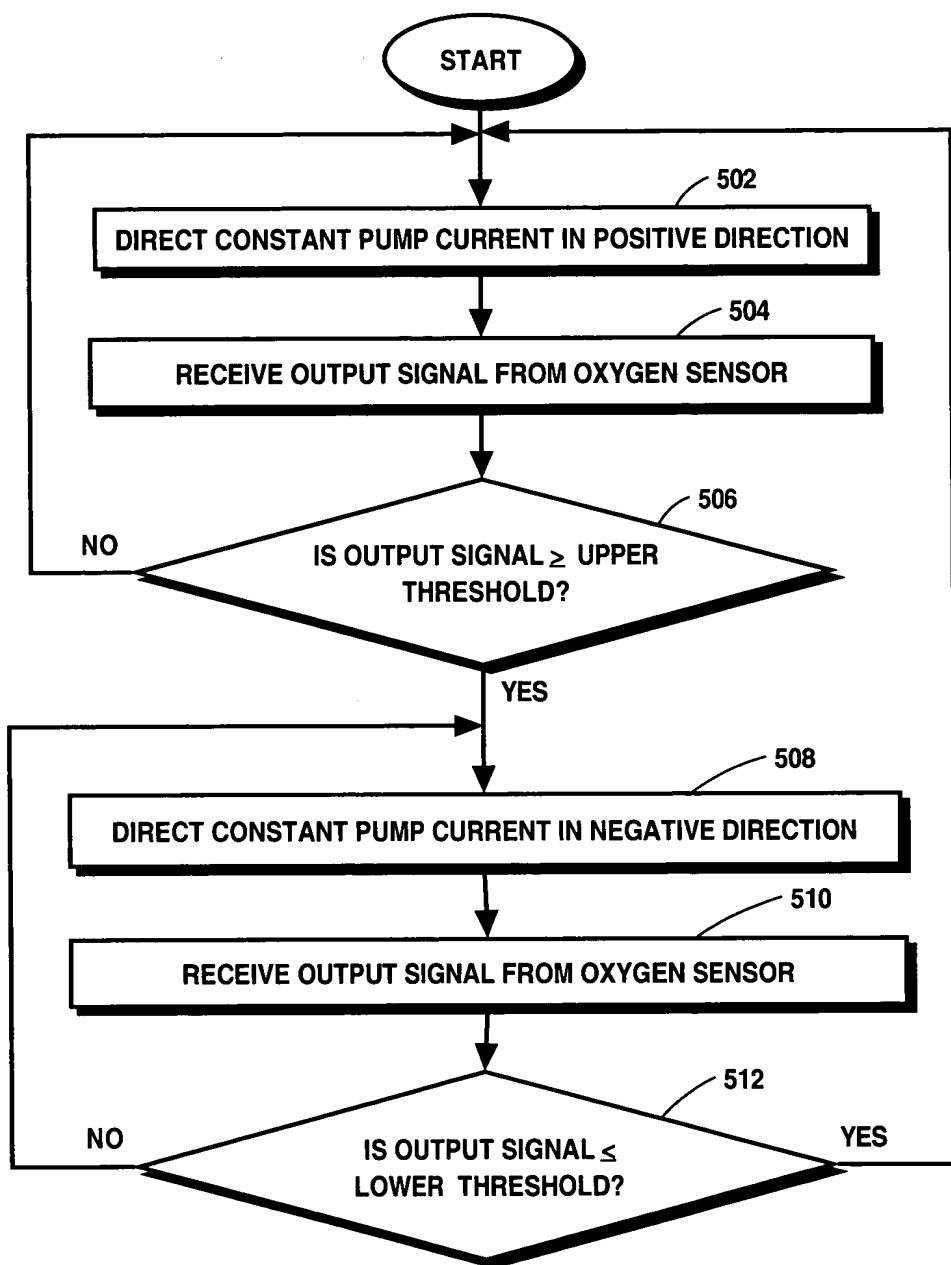


Fig. 5

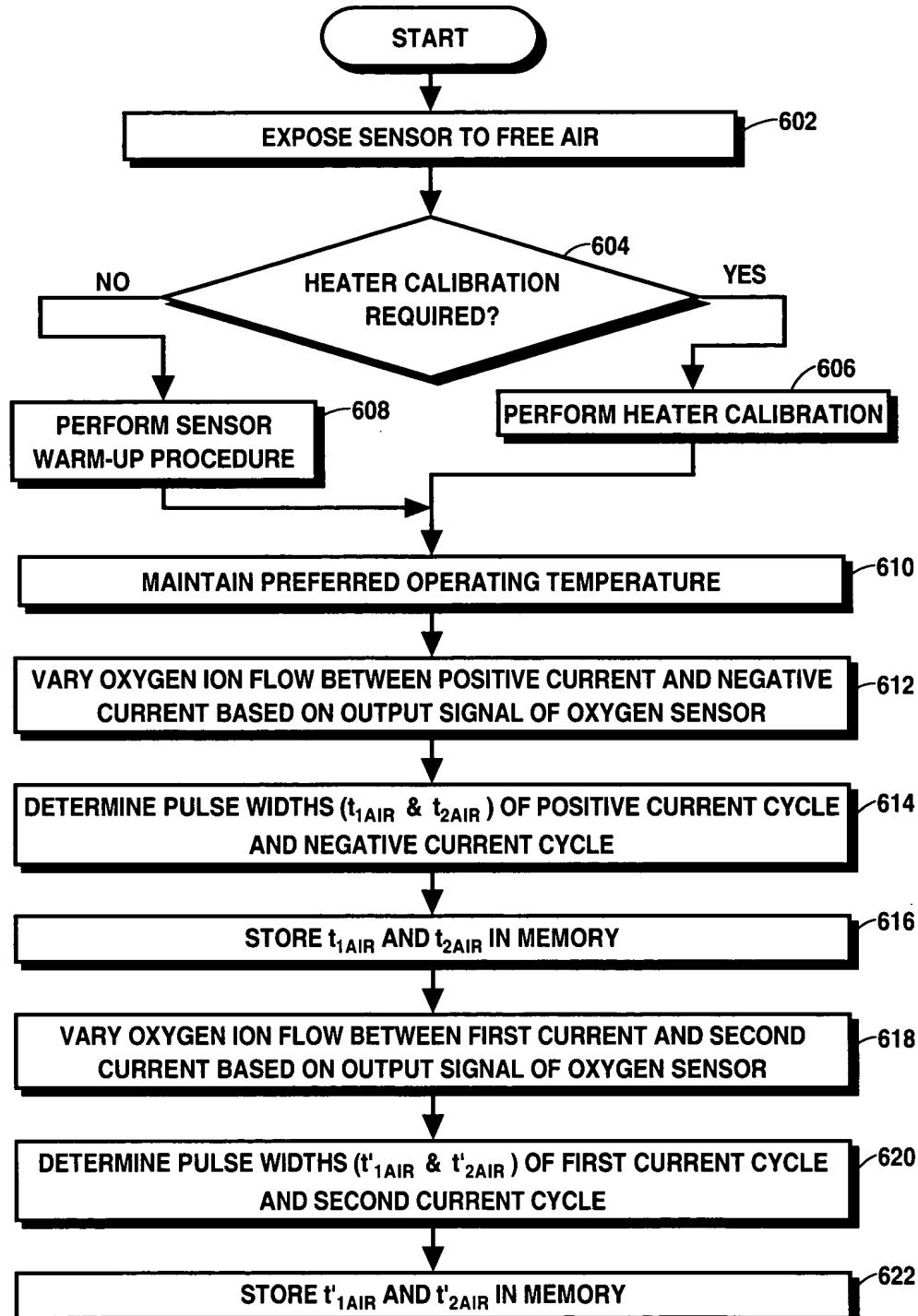


Fig. 6

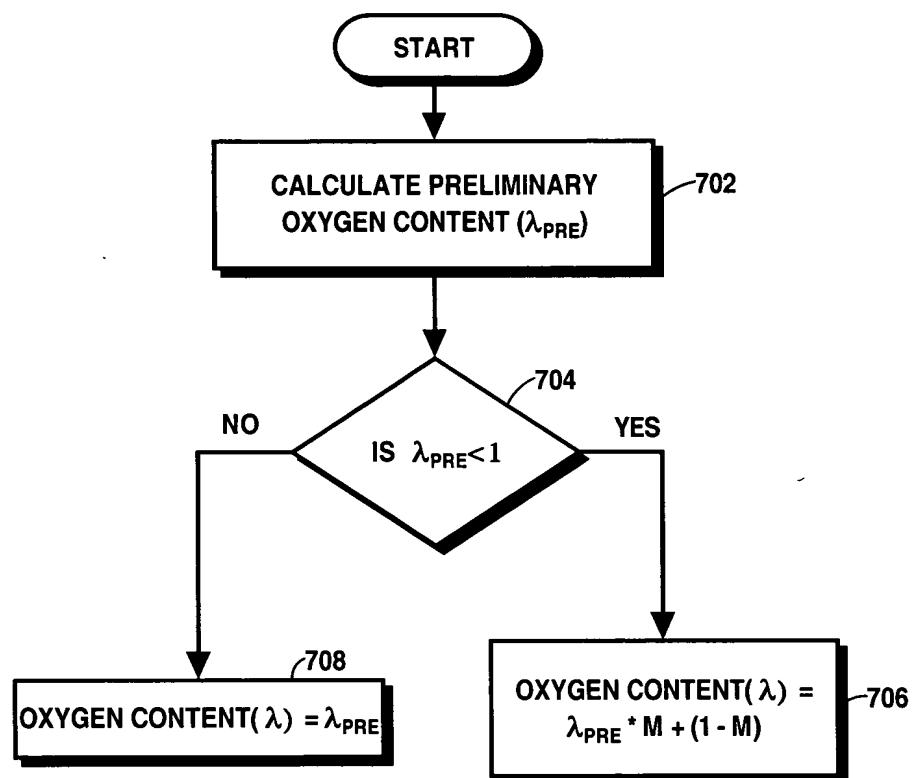


Fig. 7

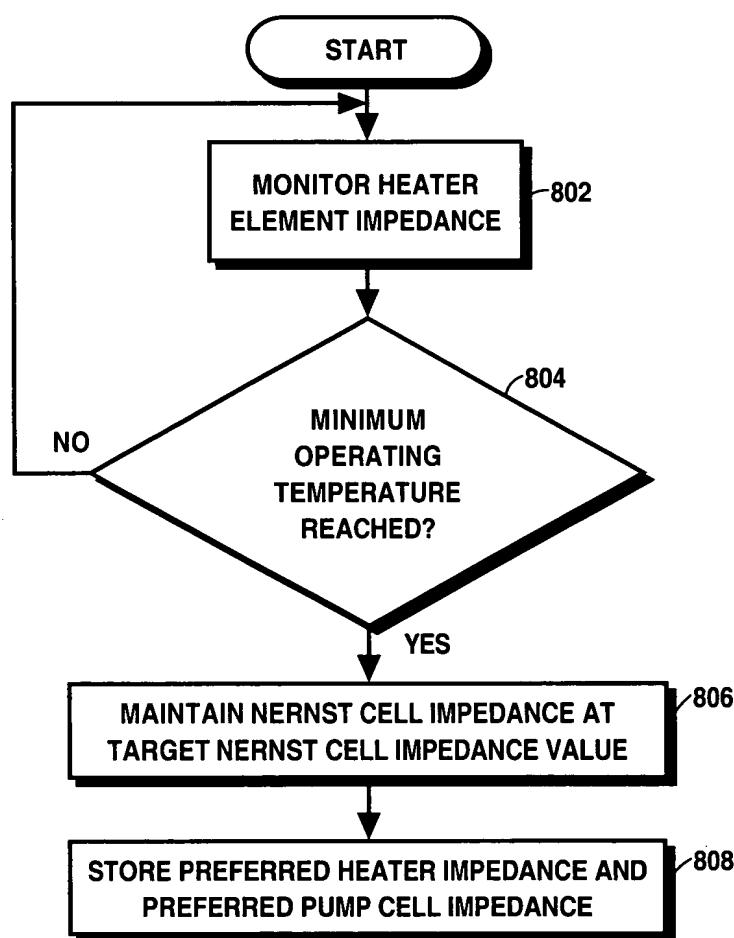


Fig. 8

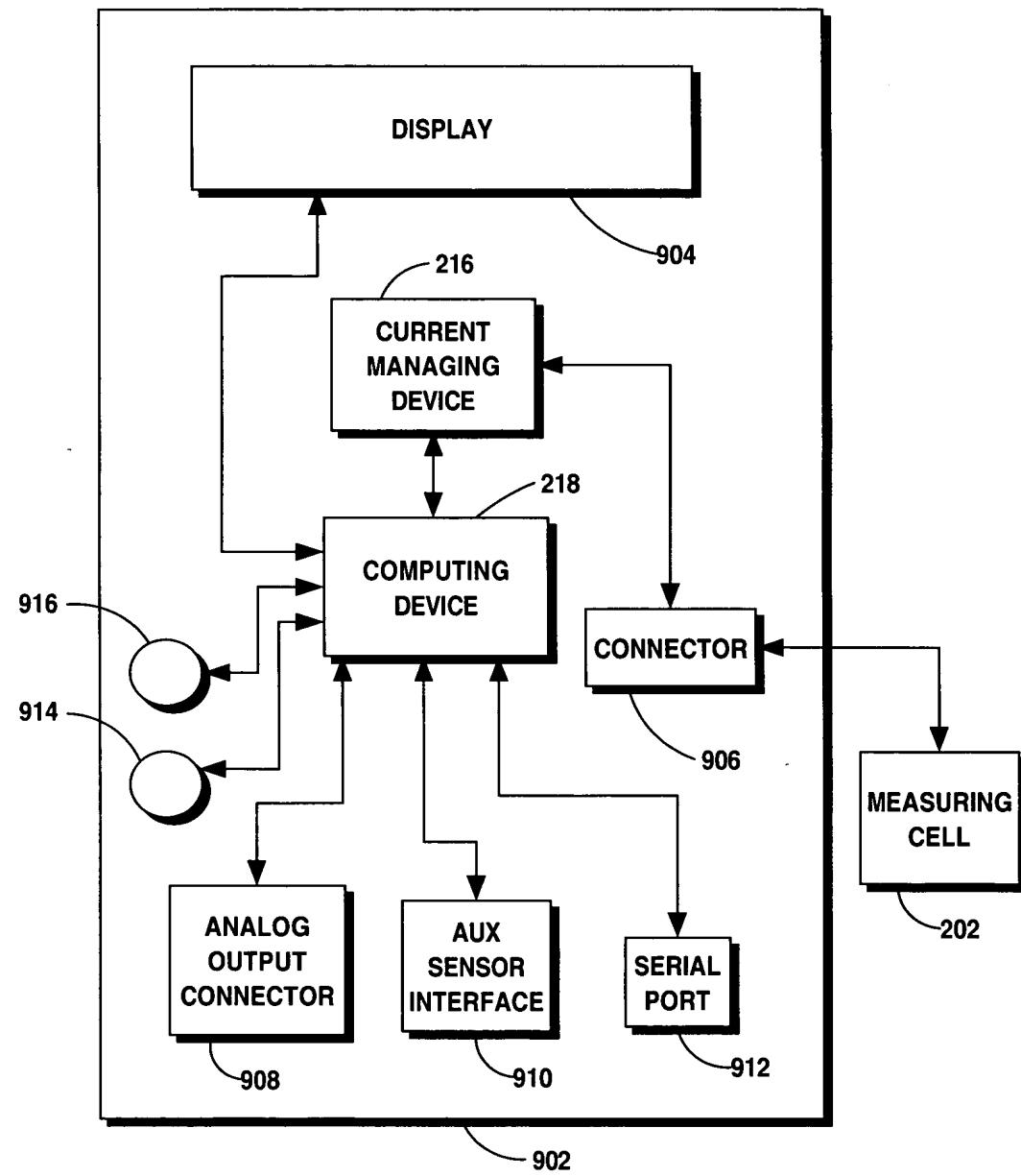


Fig. 9